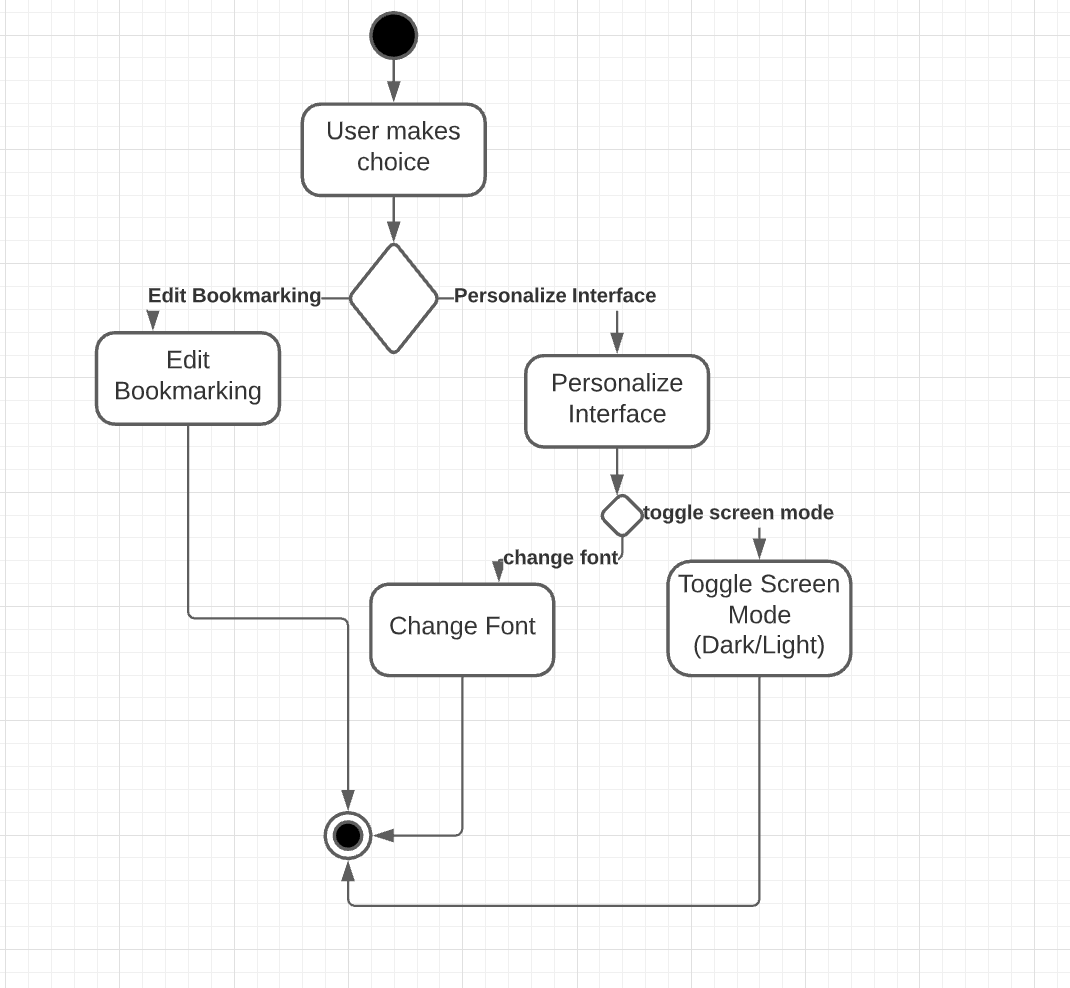
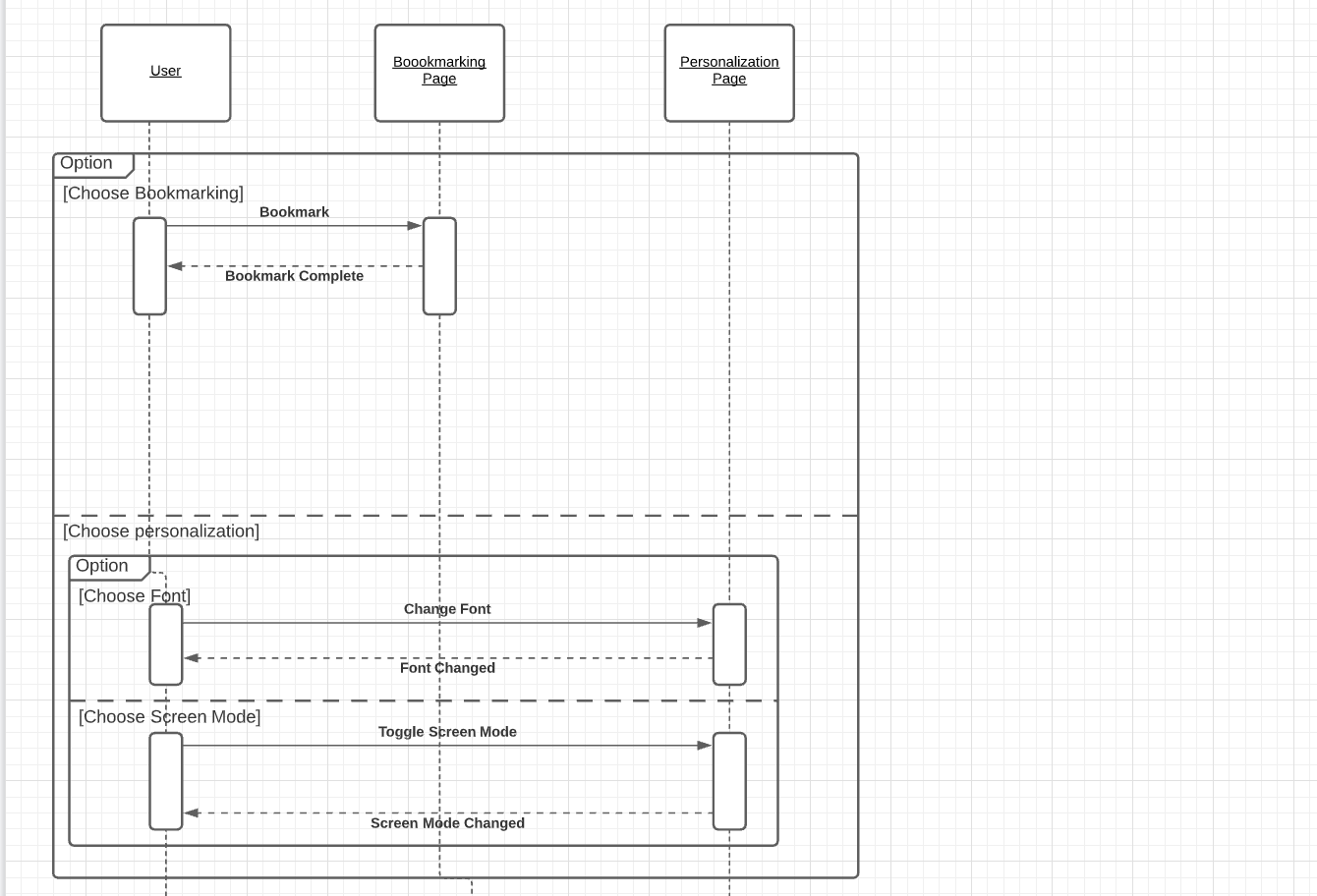
Design- phase 2

# Activity Diagram



# Sequence Diagram



# State Machine Diagram

